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RF Space SDR-IP - digital radio

£2899.94



DESCRIPTION

The high performance SDR-IP is an internet protocol (TCP/IP) software defined receiver. It offers the ultimate versatility for scientific research, radio astronomy, shortwave listening and amateur radio. The SDR-IP uses a high performance 80 MHz, 16 bit ADC with both dithering and randomization for best performance. The output I/Q bandwidth is configurable via software. PC communications are handled over a 100 base-T port using 24 or 16 bit I/Q words.

The SDR-IP is a complete plug and play solution. As opposed to other SDRs in the market, there is no need for additional pre-amplifiers, pre-selectors or code development. The unit ships with a copy of SpectraVue software that will have you up and running in minutes. For those interested in writing their own applications, RFSPACE supplies a fully documented API for communicating with the hardware. There is no need to calculate the down converter DSP parameters.

The SDR-IP just needs the center frequency, attenuator and pre-selector filter settings, output sample rate and mode to begin streaming data. Other solutions require complicated USB interfacing that is platform specific. The SDR-IP utilizes straight TCP/IP and ethernet for all communications. This offers the highest possible performance due to the highly optimized ethernet drivers on PC, Mac and Linux systems.

The SDR-IP packets are fully routable. This means that the receiver can be placed at remote locations. Multiple receivers can also be combined and it's data transported over a higher data rate ethernet pipe using simple switches. In this application, each SDR-IP can be set to a

Display: 16 x 2 Character LED LCD
Power: 5 Volts DC @ 1.5 Amps *
Connections: 4 x BNC (RF In, Ext Ref, Trigger, IF Out) , RS-232, Pulse Out, Ethernet, Power.

SDR-IP is bandwidth upgrade ready. Future PC interfaces sample rate power up to 133 MHz
Output Sample Rate = 64Mba/s @ 50 MHz Output Sample Rate = 24Mba/s Or 10 MHz Output
Sample Rate = 4.8Mba/s

Expansion: Internal Downconverter, Internal 10 MHz Phase/Frequency Lock.

The SDR-IP is housed in a solid aluminum, shielded extruded enclosure. The SDR-IP will be FCC and CE certified.

Frequency Range: 0.01 - 34 MHz *
Digital Down Converter: Xilinx FPGA
PC Interface: Ethernet 100 base-T (UDP/TCP/IP)
Filters: 120+ dB 90% Alias Free BW
Decimation Rate: Variable 40-2560 (in 10 increment)
Output Sample Rate: 32 KHz to 2.0 MHz @ 24b IQ
DDC Flatness: <0.1 dB
Dynamic Range: 105+ dB
MDS: -132dBm in 500 Hz BW
Analog to Digital Converter: 16bit w/ Dither
Digital to Analog Converter: 14bit @ 200 MHz
Preselection: 10 Filters
Attenuators: 0, -10dB, -20dB, -30dB
Sample Rate: 80.000 MHz
Memory: 65536 x 16 bit samples (256K x 16 option)
External Radio Control: Built-in RS-232 port
Dimensions: 210 x 70 x 180 mm